

Day: Saturday Date: 11/13/2004 Time: 21:03:46

Inventor Name Search Result

Your Search was:

Last Name = AKATSU

First Name = TAKESHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name 19
60472470	Not Issued	159	05/22/2003	RECYCLING BY MECHANICAL MEANS OF A WAFER COMPRISING A MULTILAYER STRUCTURE AFTER TAKING-OFF A THIN LAYER THEREOF	AKATSU, TAKESHI
<u>60472435</u>	Not Issued	159	05/22/2003	RECYCLING OF A WAFER COMPRISING A MULTI-LAYER STRUCTURE AFTER TAKING-OFF A THIN LAYER	AKATSU, TAKESHI
60472400	Not Issued	159	05/22/2003		AKATSU, TAKESHI
60439428	Not Issued	159	01/13/2003	WAFER WITH A RELAXED USEFUL LAYER AND METHOD OF FORMING THE WAFER	TAKESHI
60431930	Not Issued	159	12/09/2002	MECHANICAL RECYCLING OF A WAFER COMPRISING A BUFFER LAYER, AFTER HAVING SEPARATED A THIN LAYER THEREFROM BY MECHANICAL MEANS	AKATSU, TAKESHI
60431928	Not Issued	159	12/09/2002	RECYCLING A WAFER COMPRISING A BUFFER LAYER, AFTER HAVING SEPARATED A THIN LAYER THEREFROM	AKATSU, TAKESHI
<u>10840599</u>	Not Issued	020	05/05/2004	METHOD FOR IMPLANTING ATOMIC SPECIES THROUGH AN UNEVEN SURFACE OF A SEMICONDUCTOR LAYER	AKATSU, TAKESHI

10827437	Not Issued	030	04/20/2004	METHOD OF FABRICATION OF A SUBSTRATE FOR AN EPITAXIAL GROWTH	AKATSU, TAKESHI
10764289	Not Issued	030	01/23/2004	RECYCLING A WAFER COMPRISING A BUFFER LAYER, AFTER HAVING SEPARATED A THIN LAYER THEREFROM	AKATSU, TAKESHI
10753171	Not Issued	030	01/06/2004	COMPLIANT SUBSTRATE FOR A HETEROEPITAXIAL STRUCTURE AND METHOD FOR MAKING SAME	AKATSU, TAKESHI
10728341	Not Issued	030	12/03/2003	METHOD FOR MANUFACTURING A MULTILAYER SEMICONDUCTOR STRUCTURE THAT INCLUDES AN IRREGULAR LAYER	AKATSU, TAKESHI
10726039	Not Issued	030	12/01/2003	RECYCLING OF A WAFER COMPRISING A BUFFER LAYER AFTER HAVING SEPARATED A THIN LAYER THEREFROM BY MECHANICAL MEANS	AKATSU, TAKESHI
<u>10663917</u>	Not Issued	030	09/17/2003	WAFER WITH A RELAXED USEFUL LAYER AND METHOD OF FORMING THE WAFER	AKATSU, TAKESHI
<u>09834644</u>	Not Issued	030	04/16/2001	COMMUNICATIONS NETWORK SYSTEM USING GATEWAY	AKATSUKA, TAKESHI
<u>09675507</u>	Not Issued	061	09/29/2000	METHOD FOR THE LARGE AREA BONDING OF WAFERS	AKATSU, TAKESHI
09092055	6182171	150	06/05/1998	INFORMATION COMMUNICATION SYSTEM AND METHOD APPLICABLE TO IN -VEHICLE NETWORKING ARCHITECTURE	AKATSUKA, TAKESHI
08340851	5472909	150	11/15/1994	METHOD FOR THE PREPARATION OF DISCRETE SUBSTRATE PLATES OF SEMICONDUCTOR SILICON WAFER	AKATSUKA , TAKESHI
<u>08246401</u>	Not Issued	161	05/20/1994	METHOD FOR DRIVE-IN	AKATSUKA,

		INTO SEMICONDUCTOR SILICON WAFER	TAKESHI
08124790	5308789	METHOD OF PREPARING DIFFUSED SILICON DEVICE SUBSTRATE	:}

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Inventor Name Search Result

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Last Name = GHYSELEN

First Name = BRUNO

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60483613	Not Issued	159	06/26/2003	METHOD AND APPARATUS FOR ADJUSTING THE THICKNESS OF A THIN LAYER OF SEMICONDUCTOR MATERIAL	GHYSELEN, BRUNO
60483479	Not Issued	159	06/26/2003	METHOD FOR FORMING A RELAXED OR PSEUDO-RELAXED USEFUL LAYER ON A SUBSTRATE	GHYSELEN, BRUNO
60483476	Not Issued	159	06/26/2003	FORMING STRUCTURES THAT INCLUDE A RELAXED OR PSEUDO-RELAXED LAYER ON A SUBSTRATE	GHYSELEN, BRUNO
60472470	Not Issued	159	05/22/2003	RECYCLING BY MECHANICAL MEANS OF A WAFER COMPRISING A MULTILAYER STRUCTURE AFTER TAKING-OFF A THIN LAYER THEREOF	GHYSELEN, BRUNO
60472468	Not Issued	159	05/22/2003	METHOD FOR MANUFACTURING A HETEROGENEOUS MATERIAL STRUCTURE	GHYSELEN, BRUNO
<u>60472436</u>	Not Issued	159	05/22/2003	SEMICONDUCTOR STRUCTURE AND METHODS FOR FABRICATING SAME	GHYSELEN, BRUNO
60472435	Not Issued	159	05/22/2003	RECYCLING OF A WAFER COMPRISING A MULTI-LAYER STRUCTURE AFTER TAKING-OFF A THIN LAYER	GHYSELEN, BRUNO
60467241	Not Issued	159	04/30/2003	METHOD AND APPARATUS FOR ADJUSTING THE THICKNESS OF A LAYER OF SEMICONDUCTOR MATERIAL	GHYSELEN, BRUNO
60467240	Not Issued	159	04/30/2003	METHOD AND APPARATUS FOR ADJUSTING THE THICKNESS OF A THIN LAYER OF SEMICONDUCTOR	GHYSELEN, BRUNO

				MATERIAL	
<u>60461524</u>	Not Issued	159	04/09/2003	METHOD OF SMOOTHING THE OUTLINE OF USEFUL LAYER OF MATERIAL TRANSFERRED ONTO A SUPPORT SUBSTRATE	GHYSELEN, BRUNO
60461504	Not Issued	159		METHOD OF FABRICATING SUBSTRATES, IN PARTICULAR FOR OPTICS, ELECTRONICS OR OPTOELECTRONICS	GHYSELEN, BRUNO
60461503	Not Issued	159	04/09/2003	METHOD OF FABRICATING A DEMOUNTABLE SUBSTRATE	GHYSELEN, BRUNO
<u>60439428</u>	Not Issued	159		WAFER WITH A RELAXED USEFUL LAYER AND METHOD OF FORMING THE WAFER	GHYSELEN, BRUNO
<u>60431930</u>	Not Issued	159	12/09/2002	MECHANICAL RECYCLING OF A WAFER COMPRISING A BUFFER LAYER, AFTER HAVING SEPARATED A THIN LAYER THEREFROM BY MECHANICAL MEANS	GHYSELEN, BRUNO
60431928	Not Issued	159		RECYCLING A WAFER COMPRISING A BUFFER LAYER, AFTER HAVING SEPARATED A THIN LAYER THEREFROM	GHYSELEN, BRUNO
60413443	Not Issued	159	09/26/2002	TRANSFER OF A THIN LAYER FROM A WAFER COMPRISING A BUFFER LAYER	GHYSELEN, BRUNO
<u>10883437</u>	Not Issued	030	07/01/2004	FABRICATION OF SUBSTRATES WITH A USEFUL LAYER OF MONOCRYSTALLINE SEMICONDUCTOR MATERIAL	GHYSELEN, BRUNO
10840599	Not Issued	020	05/05/2004	METHOD FOR IMPLANTING ATOMIC SPECIES THROUGH AN UNEVEN SURFACE OF A SEMICONDUCTOR LAYER	GHYSELEN, BRUNO
<u>10827437</u>	Not Issued	030	04/20/2004	METHOD OF FABRICATION OF A SUBSTRATE FOR AN EPITAXIAL GROWTH	GHYSELEN, BRUNO
10784032	Not Issued	030	02/20/2004	METHOD OF PRODUCING A SEMICONDUCTOR STRUCTURE HAVING AT LEAST ONE SUPPORT SUBSTRATE AND AN ULTRATHIN LAYER	GHYSELEN, BRUNO
10784017	Not Issued	030	02/20/2004	METHOD FOR FORMING A RELAXED OR PSEUDO-RELAXED USEFUL LAYER ON A SUBSTRATE	GHYSELEN, BRUNO

<u>10784016</u>	Not Issued	071	02/20/2004	FORMING STRUCTURES THAT INCLUDE A RELAXED OR PSEUDO-RELAXED LAYER ON A SUBSTRATE	GHYSELEN, BRUNO
10775917	Not Issued	030	02/09/2004	METHOD OF OBTAINING A SELF-SUPPORTED THIN SEMICONDUCTOR LAYER FOR ELECTRONIC CIRCUITS	GHYSELEN, BRUNO
<u>10775865</u>	Not Issued	020	02/09/2004	APPARATUS AND METHOD FOR SPLITTING SUBSTRATES	GHYSELEN, BRUNO
<u>10764289</u>	Not Issued	030	01/23/2004	RECYCLING A WAFER COMPRISING A BUFFER LAYER, AFTER HAVING SEPARATED A THIN LAYER THEREFROM	GHYSELEN, BRUNO
10755007	Not Issued	030	01/08/2004	SUBSTRATE FOR STRESSED SYSTEMS AND METHOD OF MAKING SAME	GHYSELEN, BRUNO
10755006	Not Issued	030	01/08/2004	SUBSTRATE ASSEMBLY FOR STRESSED SYSTEMS	GHYSELEN, BRUNO
10753173	Not Issued	030	01/06/2004	INDIRECT BONDING WITH DISAPPEARANCE OF BONDING LAYER	GHYSELEN, BRUNO
<u>10733431</u>	Not Issued	030	12/10/2003	TWO-STAGE ANNEALING METHOD FOR MANUFACTURING SEMICONDUCTOR SUBSTRATES	GHYSELEN, BRUNO
10729635	Not Issued	019	12/04/2003	METHOD FOR MANUFACTURING A HETEROGENEOUS MATERIAL STRUCTURE	GHYSELEN, BRUNO
<u>10728341</u>	Not Issued	030	12/03/2003	METHOD FOR MANUFACTURING A MULTILAYER SEMICONDUCTOR STRUCTURE THAT INCLUDES AN IRREGULAR LAYER	GHYSELEN, BRUNO
10726039	Not Issued	030	12/01/2003	RECYCLING OF A WAFER COMPRISING A BUFFER LAYER AFTER HAVING SEPARATED A THIN LAYER THEREFROM BY MECHANICAL MEANS	GHYSELEN, BRUNO
<u>10704703</u>	Not Issued	071	11/12/2003	SEMICONDUCTOR STRUCTURE AND METHODS FOR FABRICATING SAME	GHYSELEN, BRUNO
10700896	Not Issued	041	11/03/2003	SEMICONDUCTOR-ON-INSULATOR STRUCTURE HAVING HIGH-TEMPERATURE ELASTIC CONSTRAINTS	GHYSELEN, BRUNO
10686084	Not Issued	030	10/14/2003	METHOD FOR CONCURRENTLY PRODUCING AT LEAST A PAIR OF SEMICONDUCTOR STRUCTURES	GHYSELEN, BRUNO

				THAT EACH INCLUDE AT LEAST ONE USEFUL LAYER ON A SUBSTRATE	
10686082	Not Issued	030	10/14/2003	PREVENTIVE TREATMENT METHOD FOR A MULTILAYER SEMICONDUCTOR STRUCTURE	GHYSELEN, BRUNO
<u>10663917</u>	Not Issued	030	09/17/2003	WAFER WITH A RELAXED USEFUL LAYER AND METHOD OF FORMING THE WAFER	GHYSELEN, BRUNO
10637094	Not Issued	071	08/06/2003	METHOD AND APPARATUS FOR ADJUSTING THE THICKNESS OF A LAYER OF SEMICONDUCTOR MATERIAL	GHYSELEN, BRUNO
<u>10637078</u>	Not Issued	030	08/06/2003	METHOD AND APPARATUS FOR ADJUSTING THE THICKNESS OF A THIN LAYER OF SEMICONDUCTOR MATERIAL	GHYSELEN, BRUNO
<u>10617521</u>	Not Issued	030	07/11/2003	METHOD OF SMOOTHING THE OUTLINE OF A USEFUL LAYER OF MATERIAL TRANSFERRED ONTO A SUPPORT SUBSTRATE	GHYSELEN, BRUNO
<u>10616594</u>	Not Issued	092	07/09/2003	METHOD OF FABRICATING SUBSTRATES, IN PARTICTULAR FOR OPTICS, ELECTRONICS OR OPTOELECTRONICS	GHYSELEN, BRUNO
<u>10615259</u>	Not Issued	041	07/09/2003	PROCESS FOR TRANSFERRING A LAYER OF STRAINED SEMICONDUCTOR MATERIAL	GHYSELEN, BRUNO
<u>10614327</u>	Not Issued	041		TRANSFER OF A THIN LAYER FROM A WAFER COMPRISING A BUFFER LAYER	GHYSELEN, BRUNO
10474984	Not Issued	041		A DETACHABLE SUBSTRATE WITH CONTROLLED MECHANICAL STRENGTH AND METHOD OF PRODUCING SAME	GHYSELEN, BRUNO
10458471	Not Issued	061		FABRICATION OF SUBSTRATES WITH A USEFUL LAYER OF MONOCRYSTALLINE SEMICONDUCTOR MATERIAL	GHYSELEN, BRUNO
10446605	6794276	150	05/27/2003	METHODS FOR FABRICATING A SUBSTRATE	GHYSELEN, BRUNO
<u>10446604</u>	Not Issued	092	. AAAAAAAAAAAAAAAAA	METHODS FOR FABRICATING FINAL SUBSTRATES	GHYSELEN, BRUNO
10349295	Not Issued	061		METHOD FOR MANUFACTURING A FREE-STANDING SUBSTRATE MADE OF MONOCRYSTALLINE SEMI-CONDUCTOR MATERIAL	GHYSELEN, BRUNO

10318304		083			GHYSELEN,
	Issued		::xxxxxxxxxxxxxxxxx	SUBSTRATES FOR MICROELECTRONICS AND	BRUNO
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				METHOD	
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10276306	_ 1100	041	::	EMBRITTLED SUBSTRATE AND	GHYSELEN,
<u>10276306</u>	Not Issued	, , , , , , , , , , , , , , , , , , , ,		EMBRITTLED SUBSTRATE AND METHOD FOR MAKING SAME	GHYSELEN, BRUNO
10276306 10239869		, , , , , , , , , , , , , , , , , , , ,		EMBRITTLED SUBSTRATE AND	

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Last Name = GHYSELEN

First Name = BRUNO

Application#	Patent#	Status	Date Filed	Title	Inventor Name 5
<u>60483476</u>	Not Issued	159	06/26/2003	FORMING STRUCTURES THAT INCLUDE A RELAXED OR PSEUDO-RELAXED LAYER ON A SUBSTRATE	GHYSELEN, BRUNO
<u>10784032</u>	Not Issued	030	02/20/2004	METHOD OF PRODUCING A SEMICONDUCTOR STRUCTURE HAVING AT LEAST ONE SUPPORT SUBSTRATE AND AN ULTRATHIN LAYER	GHYSELEN, BRUNO
<u>10784017</u>	Not Issued	030	02/20/2004	METHOD FOR FORMING A RELAXED OR PSEUDO-RELAXED USEFUL LAYER ON A SUBSTRATE	GHYSELEN, BRUNO
<u>10784016</u>	Not Issued	071	02/20/2004	FORMING STRUCTURES THAT INCLUDE A RELAXED OR PSEUDO-RELAXED LAYER ON A SUBSTRATE	GHYSELEN, BRUNO
07871537	5378683	250	04/21/1992	JOSEPHSON JUNCTION STRUCTURE	GHYSELEN , BRUNO

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